

Amendments to the Abstract:

1. Please replace the current Abstract with the following new Abstract.

ABSTRACT OF THE DISCLOSURE

A memory module has a printed circuit board with connector pins. Several Multiple different types of memory devices are mounted on the printed circuit board. An electrical circuit connects the memory devices to the connector pins such that the connector pins have multiple functionality based on the architecture of the memory devices used. For one embodiment the multiple different types of memory devices include Double Data Rate Synchronous Dynamic Random Access Memory (DDR SDRAM), a Fast Cycle Random Access Memory (FCRAM), and a Reduced Latency Dynamic Random Access Memory (RLDRAM).